1 <u>Claims</u>

2

3   What is claims is:

4

5   1. A method of dynamically adapting wireless link parameters, comprising:

6   determining a measure of errors occurring in communication over a wireless link;

7   in a case that the measure of errors corresponds to more errors than a first

8 predetermined threshold, changing from a first set of wireless link parameters to a second set of

9 wireless link parameters, the second set of wireless link parameters corresponding higher error

10 tolerance than the first set of wireless link parameters; and

11   in a case that the measure of errors corresponds to fewer errors than a second

12 predetermined threshold, changing from the first set of wireless link parameters to a third set of

13 wireless link parameters, the third set of wireless link parameters corresponding to lower error

14 tolerance than the first set of wireless link parameters.

15

16   2. A method as in claim 1, wherein the measure of errors is determined by

17 monitoring a number of NACK messages and a number of ACK messages that occur.

18

19   3. A method as in claim 2, wherein it is determined that the measure of errors

20 corresponds to more errors than the first predetermined threshold when more than a

21 predetermined number of NACK messages occur in succession.

22   4. A method as in claim 2, wherein it is determined that the measure of errors

23 corresponds to fewer errors than the second predetermined threshold when more than a

24 predetermined number of ACK messages occur in succession.

25

1   5. A method as in claim 1, wherein each set of wireless link parameters includes a

2   modulation scheme.

3

4   6. A method as in claim 1, wherein each set of wireless link parameters includes a

5   symbol rate.

6

7   7. A method as in claim 1, wherein each set of wireless link parameters includes

8   an error correction scheme.

9

10   8. A method as in claim 1, wherein each set of wireless link parameters includes a

11   modulation scheme, a symbol rate, and an error correction scheme.

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13   9. A method as in claim 8, wherein the modulation scheme is Quadrature

14   Amplitude Modulation or Quadrature Phase Shift Keying, the symbol rate is high symbol rate or

15   low symbol rate, and the error correction scheme is high forward error correction or low forward

16   error correction.

17

18   10. A method as in claim 1, wherein each set of wireless link parameters

19   corresponds to a relationship between throughput efficiency and error rate;

20   wherein the first predetermined threshold corresponds to where the relationship

21   for the first set of wireless link parameters intersects the relationship for the second set of

22   wireless link parameters; and

23   wherein the second predetermined threshold corresponds to where the relationship

24   for the first set of wireless link parameters intersects the relationship for the third set of wireless

25   link parameters.

26

1   11. An apparatus that dynamically adapts wireless link parameters, comprising:

2   a wireless link interface to a wireless link;

3   a processor; and

4   a memory storing instructions executable by the process to control communication

5 over the wireless link interface, the instructions including the steps of: (a) determining a measure

6 of errors occurring in communication over the wireless link; (b) in a case that the measure of

7 errors corresponds to more errors than a first predetermined threshold, changing from a first set

8 of wireless link parameters to a second set of wireless link parameters, the second set of wireless

9 link parameters corresponding to higher error tolerance than the first set of wireless link

10 parameters; and (c) in a case that the measure of errors corresponds to fewer errors than a second

11 predetermined threshold, changing from the first set of wireless link parameters to a third set of

12 wireless link parameters, the third set of wireless link parameters corresponding to lower error

13 tolerance than the first set of wireless link parameters.

14

15   12. An apparatus as in claim 11, wherein the measure of errors is determined by

16 monitoring a number of NACK messages and a number of ACK messages that occur.

17

18   13. An apparatus as in claim 12, wherein it is determined that the measure of

19 errors corresponds to more errors than the first predetermined threshold when more than a

20 predetermined number of NACK messages occur in succession.

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22   14. An apparatus as in claim 12, wherein it is determined that the measure of

23 errors corresponds to fewer errors than the second predetermined threshold when more than a

24 predetermined number of ACK messages occur in succession.

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15. An apparatus as in claim 11, wherein each set of wireless link parameters includes a modulation scheme.

16. An apparatus as in claim 11, wherein each set of wireless link parameters includes a symbol rate.

17. An apparatus as in claim 11, wherein each set of wireless link parameters includes an error correction scheme.

18. An apparatus as in claim 11, wherein each set of wireless link parameters includes a modulation scheme, a symbol rate, and an error correction scheme.

19. An apparatus as in claim 18, wherein the modulation scheme is Quadrature Amplitude Modulation or Quadrature Phase Shift Keying, the symbol rate is high symbol rate or low symbol rate, and the error correction scheme is high forward error correction or low forward error correction.

20. An apparatus as in claim 11, wherein each set of wireless link parameters corresponds to a relationship between throughput efficiency and error rate;

wherein the first predetermined threshold corresponds to where the relationship for the first set of wireless link parameters intersects the relationship for the second set of wireless link parameters; and

wherein the second predetermined threshold corresponds to where the relationship for the first set of wireless link parameters intersects the relationship for the third set of wireless link parameters.

1   21. A memory including instructions, the instructions executable by a processor

2   to dynamically adapt wireless link parameters, the instructions comprising the steps of:

3   determining a measure of errors occurring in communication over a wireless link;

4   in a case that the measure of errors corresponds to more errors than a first

5   predetermined threshold, changing from a first set of wireless link parameters to a second set of

6   wireless link parameters, the second set of wireless link parameters corresponding to higher error

7   tolerance than the first set of wireless link parameters; and

8   in a case that the measure of errors corresponds to fewer errors than a second

9   predetermined threshold, changing from the first set of wireless link parameters to a third set of

10   wireless link parameters, the third set of wireless link parameters corresponding to lower error

11   tolerance than the first set of wireless link parameters.

12

13   22. A memory as in claim 1, wherein the measure of errors is determined by

14   monitoring a number of NACK messages and a number of ACK messages that occur.

15

16   23. A memory as in claim 2, wherein it is determined that the measure of errors

17   corresponds to more errors than the first predetermined threshold when more than a

18   predetermined number of NACK messages occur in succession.

19

20   24. A memory as in claim 2, wherein it is determined that the measure of errors

21   corresponds to fewer errors than the second predetermined threshold when more than a

22   predetermined number of ACK messages occur in succession.

23

24   25. A memory as in claim 1, wherein each set of wireless link parameters

25   includes a modulation scheme.

26

26. A memory as in claim 1, wherein each set of wireless link parameters includes a symbol rate.

27. A memory as in claim 1, wherein each set of wireless link parameters includes an error correction scheme.

28. A memory as in claim 1, wherein each set of wireless link parameters includes a modulation scheme, a symbol rate, and an error correction scheme.

29. A memory as in claim 8, wherein the modulation scheme is Quadrature Amplitude Modulation or Quadrature Phase Shift Keying, the symbol rate is high symbol rate or low symbol rate, and the error correction scheme is high forward error correction or low forward error correction.

30. A memory as in claim 1, wherein each set of wireless link parameters corresponds to a relationship between throughput efficiency and error rate;

wherein the first predetermined threshold corresponds to where the relationship for the first set of wireless link parameters intersects the relationship for the second set of wireless link parameters; and

wherein the second predetermined threshold corresponds to where the relationship for the first set of wireless link parameters intersects the relationship for the third set of wireless link parameters.

31. An apparatus that dynamically adapts wireless link parameters, comprising:

means for determining a measure of errors occurring in communication over a wireless link; and

1   means for changing, in a case that the measure of errors corresponds to more

2   errors than a first predetermined threshold, from a first set of wireless link parameters to a second

3   set of wireless link parameters, the second set of wireless link parameters corresponding to

4   higher error tolerance than the first set of wireless link parameters, and in a case that the measure

5   of errors corresponds to fewer errors than a second predetermined threshold, from the first set of

6   wireless link parameters to a third set of wireless link parameters, the third set of wireless link

7   parameters corresponding to lower error tolerance than the first set of wireless link parameters.

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